PATENT 030439

## **CERTIFICATE OF TRANSMISSION**

I hereby certify that this correspondence (along with any paper referred to as being attached or enclosed) is being submitted *via* the USPTO EFS Filing System on the date shown below to Mail Stop Amendment, Commissioner for Patents, P.O. Box 1450, Alexandria, Virginia 22313-1450.

Date: August 7, 2008 /Gayle Gestick /
Gayle Gestick

## IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re patent application of:

Applicant(s): Jagrut Viliskumar Patel | Examiner: Aditya S. Bhat

Serial No: 10/750,342 | Art Unit: 2863

Filing Date: December 31, 2003 | Conf. No: 9469

Title: IDENTIFYING PROCESS AND TEMPERATURE OF SILICON CHIPS

Mail Stop Amendment Commissioner for Patents P.O. Box 1450 Alexandria, Virginia 22313-1450

## REPLY TO OFFICE ACTION DATED APRIL 8, 2008

Dear Sir:

Favorable reconsideration of the above-identified patent application is respectfully requested in view of the following amendments and remarks.

Applicant hereby petitions for a one (1) month Extension of Time under 37 CFR § 1.136(a) to extend the period of response from July 8, 2008 to August 8, 2008. The appropriate fees under 37 CFR § 1.17 are submitted herewith.